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## 1   Special issue: AI in engineering

D. Sriram, R. Joobbani
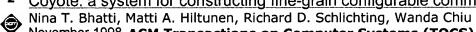April 1985 **ACM SIGART Bulletin**,  Issue 92
**Publisher:** ACM Press
Full text available: 🗎 pdf(8.79 MB)      Additional Information: full citation, abstract

> The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

## 2   Coyote: a system for constructing fine-grain configurable communication services

Nina T. Bhatti, Matti A. Hiltunen, Richard D. Schlichting, Wanda Chiu
November 1998 **ACM Transactions on Computer Systems (TOCS)**,  Volume 16 Issue 4
**Publisher:** ACM Press
Full text available: 🗎 pdf(290.21 KB)    Additional Information: full citation, abstract, references, citings, index terms

> Communication-oriented abstractions such as atomic multicast, group RPC, and protocols for location-independent mobile computing can simplify the development of complex applications built on distributed systems. This article describes Coyote, a system that supports the construction of highly modular and configurable versions of such abstractions. Coyote extends the notion of protocol objects and hierarchical composition found in existing systems with support for finer-grain microprotocol ob ...

> **Keywords**: x-kernal, configurable sevices, customization, event handlers, event-driven execution, membership, microprotocols, mobile computing, modularity, multicast, protocols, remote procedure call

## 3   Distributed operating systems

Andrew S. Tanenbaum, Robbert Van Renesse
December 1985 **ACM Computing Surveys (CSUR)**,  Volume 17 Issue 4
**Publisher:** ACM Press
Full text available: 🗎 pdf(5.49 MB)      Additional Information: full citation, abstract, references, citings, index terms, review

Distributed operating systems have many aspects in common with centralized ones, but they also differ in certain ways. This paper is intended as an introduction to distributed operating systems, and especially to current university research about them. After a discussion of what constitutes a distributed operating system and how it is distinguished from a computer network, various key design issues are discussed. Then several examples of current research projects are examined in some detail ...

**4**  TinyDB: an acquisitional query processing system for sensor networks
Samuel R. Madden, Michael J. Franklin, Joseph M. Hellerstein, Wei Hong
March 2005 **ACM Transactions on Database Systems (TODS)**, Volume 30 Issue 1
**Publisher:** ACM Press
Full text available: pdf(1.67 MB)      Additional Information: full citation, abstract, references, index terms

We discuss the design of an acquisitional query processor for data collection in sensor networks. Acquisitional issues are those that pertain to where, when, and how often data is physically acquired (*sampled*) and delivered to query processing operators. By focusing on the locations and costs of acquiring data, we are able to significantly reduce power consumption over traditional passive systems that assume the a priori existence of data. We discuss simple extensions to SQL for controlli ...

**Keywords**: Query processing, data acquisition, sensor networks

**5**  Experience Using Multiprocessor Systems—A Status Report
Anita K. Jones, Peter Schwarz
June 1980 **ACM Computing Surveys (CSUR)**, Volume 12 Issue 2
**Publisher:** ACM Press
Full text available: pdf(4.48 MB)      Additional Information: full citation, references, citings, index terms

**6**  Sensor databases: The design of an acquisitional query processor for sensor networks
Samuel Madden, Michael J. Franklin, Joseph M. Hellerstein, Wei Hong
June 2003 **Proceedings of the 2003 ACM SIGMOD international conference on Management of data**
**Publisher:** ACM Press
Full text available: pdf(485.52 KB)      Additional Information: full citation, abstract, references, citings, index terms

We discuss the design of an acquisitional query processor for data collection in sensor networks. Acquisitional issues are those that pertain to where, when, and how often data is physically acquired (*sampled*) and delivered to query processing operators. By focusing on the locations and costs of acquiring data, we are able to significantly reduce power consumption over traditional passive systems that assume the *a priori* existence of data. We discuss simple extensions to SQL for co ...

**7**  SPOTS'06 session 4--new sensors and architectures: The low power energy aware processing (LEAP)embedded networked sensor system
Dustin McIntire, Kei Ho, Bernie Yip, Amarjeet Singh, Winston Wu, William J. Kaiser
April 2006 **Proceedings of the fifth international conference on Information processing in sensor networks IPSN '06**
**Publisher:** ACM Press
Full text available: pdf(200.80 KB)   Additional Information: full citation, abstract, references, index terms

A broad range of embedded networked sensor (ENS) systems for critical environmental

monitoring applications now require complex, high peak power dissipating sensor devices, as well as on-demand high performance computing and high bandwidth communication. Embedded computing demands for these new platforms include support for computationally intensive image and signal processing as well as optimization and statistical computing. To meet these new requirements while maintaining critical support for ...

**Keywords**: embedded wireless networked sensor, energy-aware multiprocessor platform, sensor platform hardware and software architecture

**8** Cluster-based scalable network services
Armando Fox, Steven D. Gribble, Yatin Chawathe, Eric A. Brewer, Paul Gauthier
October 1997 **ACM SIGOPS Operating Systems Review , Proceedings of the sixteenth ACM symposium on Operating systems principles SOSP '97**, Volume 31 Issue 5
**Publisher:** ACM Press
Full text available: pdf(2.42 MB)   Additional Information: full citation, references, citings, index terms

**9** Link and channel measurement: A simple mechanism for capturing and replaying wireless channels
Glenn Judd, Peter Steenkiste
August 2005 **Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to wireless network design and analysis E-WIND '05**
**Publisher:** ACM Press
Full text available: pdf(6.06 MB)   Additional Information: full citation, abstract, references, index terms

Physical layer wireless network emulation has the potential to be a powerful experimental tool. An important challenge in physical emulation, and traditional simulation, is to accurately model the wireless channel. In this paper we examine the possibility of using on-card signal strength measurements to capture wireless channel traces. A key advantage of this approach is the simplicity and ubiquity with which these measurements can be obtained since virtually all wireless devices provide the req ...

**Keywords**: channel capture, emulation, wireless

**10** An IPC protocol and its hardware realization for a high-speed distributed multicomputer system
W. K. Giloi, P. Behr
May 1981 **Proceedings of the 8th annual symposium on Computer Architecture**
**Publisher:** IEEE Computer Society Press
Full text available: pdf(744.71 KB)   Additional Information: full citation, abstract, references, citings, index terms

Multicomputer systems with distributed control form an architecture that simultaneously satisfies such design goals as high performance through parallel operation of VLSI processors, modular extensibility, fault tolerance, and system software simplication. The nodes of the system may be locally concentrated or spatially dispersed as a local network. Applications range from data base-oriented transactional systems to "number crunching." The system is service-oriented; that is, it ...

**11** A SMART scheduler for multimedia applications
Jason Nieh, Monica S. Lam
May 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 2

**Publisher:** ACM Press

Full text available: pdf(570.87 KB)   Additional Information: full citation, abstract, references, index terms

Real-time applications such as multimedia audio and video are increasingly populating the workstation desktop. To support the execution of these applications in conjunction with traditional non-real-time applications, we have created SMART, a Scheduler for Multimedia And Real-Time applications. SMART supports applications with time constraints, and provides dynamic feedback to applications to allow them to adapt to the current load. In addition, the support for real-time applications is integrat ...

**Keywords:** Scheduling, multimedia, proportional sharing, real-time

**12** Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

**Publisher:** IBM Press

Full text available: pdf(4.21 MB)     Additional Information: full citation, abstract, references, index terms

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

**13** Tools and methods for the verification of processors and processor-based systems: Smart diagnostics for configurable processor verification

Sadik Ezer, Scott Johnson

June 2005 **Proceedings of the 42nd annual conference on Design automation**

**Publisher:** ACM Press

Full text available: pdf(228.59 KB)   Additional Information: full citation, abstract, references, index terms

This paper describes a novel technique called Embedded Test-bench Control (ETC), extensively used in the verification of Tensilica's latest configurable processor. Conventional simulation-based verification methodologies that employ assembly programs for testing cannot easily link the diagnostic program to the test-bench for interactive control, consequently resulting in weaker coverage. ETC links the diagnostic program execution and the test-bench functions, thereby increasing the flexibility a ...

**Keywords:** configurable processors, coverage, diagnostics, embedded test-bench control, functional verification

**14** A parallel embedded-processor architecture for ATM reassembly

Richard F. Hobson, P. S. Wong

February 1999 **IEEE/ACM Transactions on Networking (TON)**, Volume 7 Issue 1

**Publisher:** IEEE Press

Full text available: pdf(331.21 KB)   Additional Information: full citation, references, citings, index terms

**Keywords:** ATM, embedded systems, medium access control, segmentation and reassembly

**15** Power reduction techniques for microprocessor systems

Vasanth Venkatachalam, Michael Franz

September 2005 **ACM Computing Surveys (CSUR)**, Volume 37 Issue 3

**Publisher:** ACM Press

Full text available: pdf(602.33 KB)   Additional Information: full citation, abstract, references, index terms

> Power consumption is a major factor that limits the performance of computers. We survey the "state of the art" in techniques that reduce the total power consumed by a microprocessor system over time. These techniques are applied at various levels ranging from circuits to architectures, architectures to system software, and system software to applications. They also include holistic approaches that will become more important over the next decade. We conclude that power management is a ...

> **Keywords**: Energy dissipation, power reduction

**16** SPOTS'06 session 3--software, development suites, & algorithms: Sdlib: a sensor network data and communications library for rapid and robust application development

David Chu, Kaisen Lin, Alexandre Linares, Giang Nguyen, Joseph M. Hellerstein

April 2006 **Proceedings of the fifth international conference on Information processing in sensor networks IPSN '06**

**Publisher:** ACM Press

Full text available: pdf(466.99 KB)   Additional Information: full citation, abstract, references, index terms

> Sensor network applications tend to exhibit significant high-level commonalities along several major dimensions that have heretofore been underexposed, particularly in the areas of collection and dissemination. We have developed a component library, sdlib, which presents the fundamental abstractions of collection and dissemination as part of a dataflow sytem. This allows application developers to rapidly develop applications at the *nesC* level. This means that sdlib maintains significant e ...

> **Keywords**: collection, dissemination, software library, wireless sensor networks

**17** Executable requirements for embedded systems

Pamela Zave, Raymond T. Yeh

March 1981 **Proceedings of the 5th international conference on Software engineering**

**Publisher:** IEEE Press

Full text available: pdf(938.54 KB)   Additional Information: full citation, abstract, references, citings, index terms

> An approach to requirements specification for embedded systems, based on constructing an executable model of the proposed system interacting with its environment, is proposed. The approach is explained, motivated, and related to data-oriented specification techniques. Portions of a specification language embodying it are introduced, and illustrated with an extended example in which the requirements for a process-control system are developed incrementally.

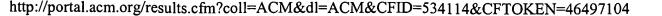**18** The Alpha demonstration unit: a high-performance multiprocessor

Charles P. Thacker, David G. Conroy, Lawrence C. Stewart

February 1993 **Communications of the ACM**, Volume 36 Issue 2

**Publisher:** ACM Press

Full text available: pdf(6.26 MB)   Additional Information: full citation, references, citings, index terms, review

**Keywords**: Alpha AXP chip

**19** The Clipper processor: instruction set architecture and implementation

W. Hollingsworth, H. Sachs, A. J. Smith
February 1989 **Communications of the ACM**, Volume 32 Issue 2
**Publisher:** ACM Press

Full text available: pdf(4.67 MB)    Additional Information: full citation, abstract, references, citings, index terms, review

Intergraph's CLIPPER microprocessor is a high performance, three chip module that implements a new instruction set architecture designed for convenient programmability, broad functionality, and easy future expansion.

**20** Embra: fast and flexible machine simulation

Emmett Witchel, Mendel Rosenblum
May 1996 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1996 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '96**, Volume 24 Issue 1
**Publisher:** ACM Press

Full text available: pdf(1.83 MB)    Additional Information: full citation, abstract, references, citings, index terms

This paper describes Embra, a simulator for the processors, caches, and memory systems of uniprocessors and cache-coherent multiprocessors. When running as part of the SimOS simulation environment, Embra models the processors of a MIPS R3000/R4000 machine faithfully enough to run a commercial operating system and arbitrary user applications. To achieve high simulation speed, Embra uses dynamic binary translation to generate code sequences which simulate the workload. It is the first machine simu ...

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